

## code segment from cfg\_ser\_in\_v20.vhd

```

if ( RESET = '1' ) then
    state <= idle;
    DATA_RDY <= '0';
    par_reg <= (others => '0');
elsif (rising_edge(CFGCLK)) then
    case state is
        when idle =>
            DATA_RDY <= '0';
            par_reg <= (others => '0');
            if (SDATA_ENABLE_N = '0') then
                par_reg <= par_reg(14 downto 0) & SDATA_IN;
                state <= rx_data;
            else
                state <= idle;
            end if;
        when rx_data =>
            if (SDATA_ENABLE_N = '0') then
                par_reg <= par_reg(14 downto 0) & SDATA_IN;
                state <= rx_data;
            else
                DATA_RDY <= '1';
                state <= idle;
            end if;
    end case;
end if;

```

